Aliasghar Salehpour

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Education

- **University of Regina** **2020-Now**

Ph.D. degree, Electronic Systems Engineering, Regina, GPA: 87.5/100

Supervisor: Dr. Irfan Al-Anbagi

- **TEHRAN UNIVERSITY** **2006-2009**

M.S., Computer Engineering (Computer Architecture), Tehran, Iran, GPA: 85/100

(Assessed by WES, Reference No.: 3220677)

Supervisor: Dr. Ali Afzali-Kousha

Thesis: Investigating and Improving Performance of Routing Protocols for Wireless Sensor Network

SHAHED UNIVERSITY **2001-2006**

B.S., Computer Engineering (Hardware Engineering), Tehran, Iran, GPA: 78/100

(Assessed by WES, Reference No.: 3220677)

Al e AGHA HIGH SCHOOL **1997-2001**

Diploma in Mathematics and Physics, Tehran, Iran. GPA: 3.71/4

➢ Teaching Experience

- Teacher Assistant, Design of Computer Networks, University of Regina **Winter 2021**
- Teacher Assistant, Logic Circuits, University of Tehran **Fall 2008**
- Instructor, Logic Circuits Lab, University of Tehran **Fall 2008**
- Teacher Assistant, Computer Networks, University of Tehran **Winter 2007**

➢ Honors

- Ranked 8th in national university entrance exam for M.Sc degree in Computer Engineering in 2006.
- The organizer of the first technical workshop on testing of digital circuits at Tehran University (iCDT2007)
- Evaluation of master thesis as a valuable piece of work by Iran Telecommunication Research Institute

➢ Publications

- **Conference Papers**

1. **Ali-Asghar Salehpour**, Masoud Zamani, Amir-Mohammad Rahmani, Siamak Mohammadi, "A Novel Test Environment for Template based QDI Asynchronous Circuits," in Proc. of 15th IEEE International Conference on Electronics, Circuits, and Systems (ICECS 2008), 2008, Malta.
2. Amir-Mohammad Rahmani, **Ali-Asghar Salehpour**, Masoud Zamani, Siamak Mohammadi, "An Efficient Fault Simulator for Template based QDI Asynchronous Circuits," in Proc. of 4th IEEE Southern Conference on Programmable Logic, pp. 99-105, 2008, Argentina.
3. Pejman Lotfi-Kamran, Amir-Mohammad Rahmani, **Ali-Asghar Salehpour**, Ali Afzali-Kusha, and Zainalabedin Navabi, "Stall Power Reduction in Pipelined Architecture Processors," In Proc. of 21st IEEE International Conference on VLSI Design, pp. 541-546, 2008, India.
4. **Ali-Asghar Salehpour**, Ali Afzali-Kusha, Siamak Mohammadi, "Memetic Algorithm for clustering in Wireless Sensor Networks," in Proc. of 5th International Conference on Innovations in Information Technology, Al-Ain, 2008.
5. **Ali-Asghar Salehpour**, Ali Afzali-Kusha, Siamak Mohammadi, "AN ENERGY EFFICIENT ROUTING PROTOCOL FOR CLUSTER-BASED WIRELESS SENSOR NETWORKS USING ANT COLONY OPTIMIZATION," in Proc. of 5th International Conference on Innovations in Information Technology, Al-Ain, 2008.

- **Journal Paper**

  Pejman Lotfi-Kamran, **Ali-Asghar Salehpour**, Ali Afzali-Kousha, and Zainalabedin Navabi, "Dynamic Power Reduction of Stalls in Pipeline Architecture Processors," International Journal of Design, Analysis and Tools for Integrated Circuits and Systems (IJDATICS)*, Vol. 1, No. 1, pp. 9-15, June2011.*

➢ Professional Experience (Employment)

- Working in Shahr Private Bank as a wireless network engineer and computer systems administrator 2012-2020
- Activity as an expert in modern banking services at Shahr Private Bank 2011-2012
- Activity in the agricultural engineering research institute as a network expert and Project Manager 2008-2009
- Project manager of "Design and Implementation of a Network Security Model for Agricultural Engineering Research Institute" 2006-2007
- Project manager of "Implementation of Computer Simulator of Witches' Broom Disease of Lime trees (WBDL)" at agricultural engineering research institute 2007-2008
- Designing a tool for testing of digital circuits at university of Tehran 2006
- Technical assistance for the implementation of IP Telephony project at Iran Telecommunication Research Institute (ITRC) 2004

➢ Technical Skills

- Sound knowledge in networking protocols and wireless systems
- Extensive experience in Routing Protocols
- Experience in configuring and maintaining routers, switches and wireless network switches
- Experience in Network tools:
  - OMNeT, XMulator, ns-2, OPNET, Mannasim.
- Experience in other tools:
  - Matlab, LaTeX
- Experience in the field of network security
- Good Conceptual, Analytical and Logical skills
- Ability to work individually as well as in group environment

- Experience in designing, developing, implementing and testing computer-based hardware and software
- Proficient in design networks that connect computer systems
- Strong motivational and leadership skills
- Ability to work under pressure
- Strong Analytical and troubleshooting skills
- Experience in Project management
- Experience in creating project management plans, schedules, cost estimates
- Extensive experience in VHDL- and VerilogHDL-based hardware design.
- Experience in high-level VHDL-based modeling of systems.
- Experience in SystemC-based hardware design.
- Experience in programming with Assembly Language, C, Object Oriented Programming, Borland C++, Visual C++, C#, Delphi.
- Experience in operating system: UNIX, Linux, Windows.
- Experience in many CAD tools:
    - "Modelsim", "Synplify", "Quartus", "Leonardo Spectrum", "Active HDL", "Xilinx ISE", "Xilinx XPower".
    - "Tanner LEdit", "SYNOPSYS Design Analyzer", etc.
- Experience in Asynchronous Design and its CAD tool:
    - "Balsa", "Petrify", "Persia".
- Experience in Low Power Design and Low Power Architectures.
- Experience in verification of digital circuits and some tools to this purpose:
    - "PLI", "OVL", "PSL", "SystemC".

➢ Certifications

- Member of APEGS as Engineer-In-Training (ID: 42751, From: 2019/02/27)
- Network+
- CCNA
- Security+
- Windows Form Programming in C#

➢ Languages

- Persian Mother Tongue
- English: IELTS overall band score of 6.5; Listening 6.0; Speaking 6.5; Writing 6.0; Reading 6.5.